



# 7x5mm LVDS O/P TCXO/VC-TCXO

## 10.0 MHz to 800.0 MHz Low Phase Noise

AEL MWD430-Series TCXO  
AEL VMWD430-Series VC-TCXO



### Operating Conditions

Operating Temperature Range		Frequency Stability (PPM)					
		±1.0	±2.00	±2.5	±3.0	±4.0	±5.0
Standard	-0°C to +70°C	✓	✓	✓	✓	✓	✓
Industrial	-30°C to +75°C	X	✓	✓	✓	✓	✓

<b>Storage Temp</b>	-55°C to +125°C
<b>Option Codes</b>	
<b>Supply Voltage</b>	Option Code
+3.3V DC	3

### Marking & Specification Code Format

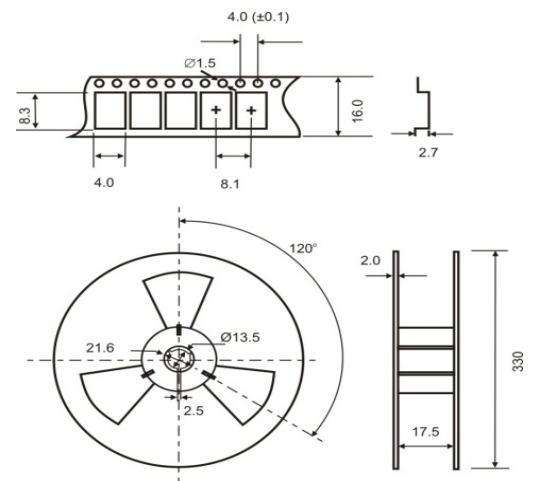
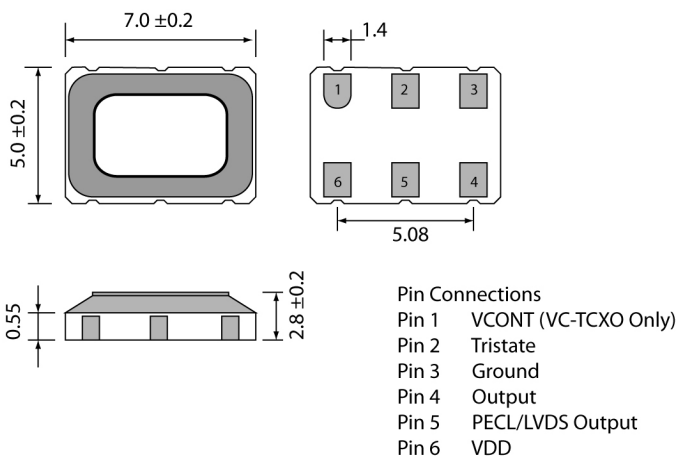
Type	Pltage Code	OTR/Stability	Frequency	Pulling	WWYY
V/MWD431	3	See Above	ie 27.000	in PPM	1608

Other temp. ranges/stabilities available

Parameter	Condition	< 24 MHz	24.01 MHz -	96.01 MHz -
			96.000 MHz	800.00 MHz
Input Current	15pF Load	25mA Max.	65mA Max.	100mA Max.
Frequency Stability	Over Temperature	See Options Above		
	vs Ageing	≤ ±1.0PPM 1st Year at 25°C		
	vs Voltage Change	≤ ±0.3PPM for a ±5% input voltage change		
	vs Load Change	≤ ±0.3PPM for a ±10% load change		
	vs Reflow	≤ ±1.0PPM 260°C reflow after 24hrs		
Symmetry	at 1.25V	45/55%		
Output Voltage	"0" Level	0.9V Min. (1.1V Typical)		
	"1" Level	1.4V Typical (1.6V Max.)		
Differential O/P Voltage V <sub>OD</sub>		247mV Min. - 355mV Typ. - 454mV Max. O/P 1 & 2		
Differential O/P Error dV <sub>OD</sub>		-50mV Min. - 50mV Max		
O/P Offset Voltage V <sub>OS</sub>		1.125V Min. - 1.20V Typ. - 1.375V Max.		
Offset Magnitude Error dV <sub>OS</sub>		0mV Min. - 3mV Typ. - 25mV Max.		
Rise/Fall Time	20% to 80% of LVDS Waveform	1.5ns Max		
Start Up Time	0V to V <sub>DD</sub>	5ms Typ. - 10ms Max.		
Voltage Control VMWD43 Only	Control Voltage	+1.5V ±1.0V		
	Pulling Range	Standard	±5.0PPM to ±12PPM (To be specified)	
		Narrow	±1.0PPM Max. (or custom)	
Load		50Ω from each Output		
Jitter at 155.52 MHz	Period Jitter (RMS)	5ps Typical		
	Period Jitter (Peak to Peak)	28ps Typical		
Phase Noise at 155.520 MHz	100 kHz Offset	-119dBc/Hz		

### Dimensions (mm)

1000pcs/Reel



Static sensitive device